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## What is claimed is:

A communications receiver, comprising:

- a low noise amplifier (LNA) for amplifying a received signal so as to generate an LNA output signal, said LNA having M gain setting modes of operation, the gain of said LNA determined in response to an LNA gain control command;
- a mixer for multiplying said LNA output signal with a local oscillator signal so as to generate a mixer output signal, said mixer having N gain setting modes of operation, the gain of said mixer determined in response to a mixer gain control signal command;
- a detector for recovering, from said mixer output signal, information originally transmitted; and
- a controller operative to generate said gain control command to said LNA and said mixer gain control command to said mixer, said controller adapted to set the gain setting of said LNA and of said mixer to one of a plurality of gain states, wherein each gain state consists of a unique combination of LNA gain setting and mixer gain setting.
- 2. The receiver according to claim 1, wherein said LNA gain control command comprises a plurality of log<sub>2</sub>M control lines, wherein M is a positive integer.
- 3. The receiver according to claim 2, wherein M equals two.
- 20 4. The receiver according to claim 1, wherein said mixer gain control command comprises a plurality of log<sub>2</sub>N control lines, wherein N is a positive integer.
  - 5. The receiver according to claim 4, wherein N equals two.
  - 6. The receiver according to claim 1, further comprising a band pass filter located before said LNA and adapted to filter the signal received from an antenna.
- 7. The receiver according to claim 1, further comprising a band pass filter located before said mixer and adapted to filter the LNA output signal before input to said mixer.
  - 8. The receiver according to claim 1, further comprising a band pass filter located after said mixer and adapted to filter said mixer output.

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- 9. The receiver according to claim 1, wherein said detector comprises: a limiter adapted to limit the amplitude swing of a signal input thereto; a discriminator adapted to generate an output voltage proportional to frequency deviations in the output of said limiter; and a data slicer operative to apply a threshold to the output voltage of said discriminator so as to recover digital data therefrom. A communications receiver, comprising: a low noise amplifier (LNA) for amplifying a received signal so as to generate an LNA output signal, said LNA having a low gain and a high gain mode of operation, the gain of said LNA determined in response to an LNA gain control command; a mixer for multiplying said LNA output signal with a local oscillator signal so as to generate a mixer output signal, said mixer having a low gain and a high gain mode of operation, the gain of said mixer determined in response to a mixer gain control command; a detector for recovering, from said mixer output signal, information originally transmitted; and a controller operative to generate said LNA gain control command and said mixer gain control command, said controller adapted to set the gain setting of said LNA and of said thixer to one of four gain states, wherein each gain state consists of a unique combination of LNA gain setting and mixer gain setting. The receiver according to claim 10, further comprising a band pass filter located 11. before said LNA and adapted to filter the signal received from an antenna. 12. The receiver according to claim 10, further comprising a band pass filter located before said mixer and adapted to filter the LNA output signal before input to said mixer. 13. The receiver according to claim 10, further comprising a band pass filter located after
- 14. The receiver according to claim 10, wherein said detector comprises:

said mixer and adapted to filter said mixer output.

- a limiter adapted to limit the amplitude swing of a signal input thereto;
- a discriminator adapted to generate an output voltage proportional to frequency deviations in the output of said limiter; and

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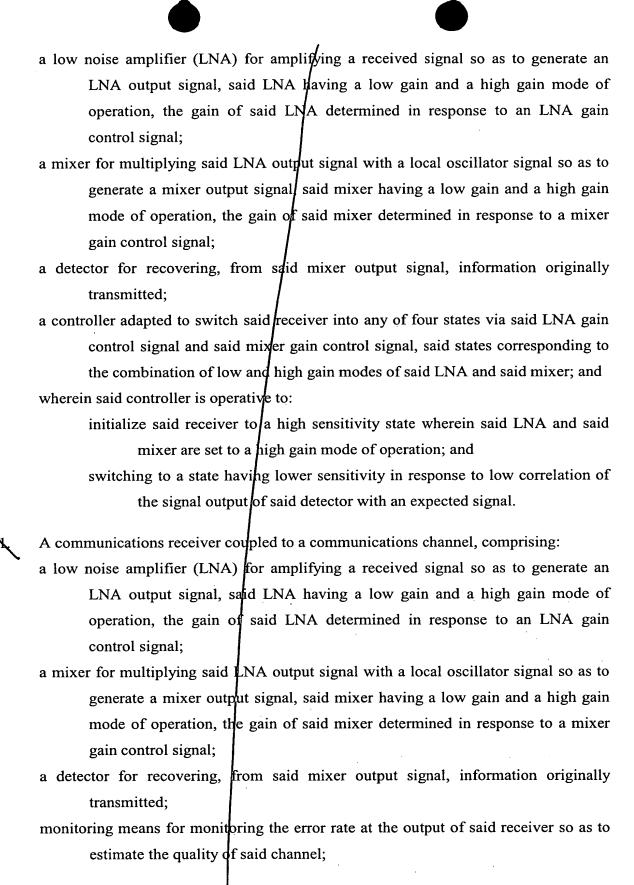
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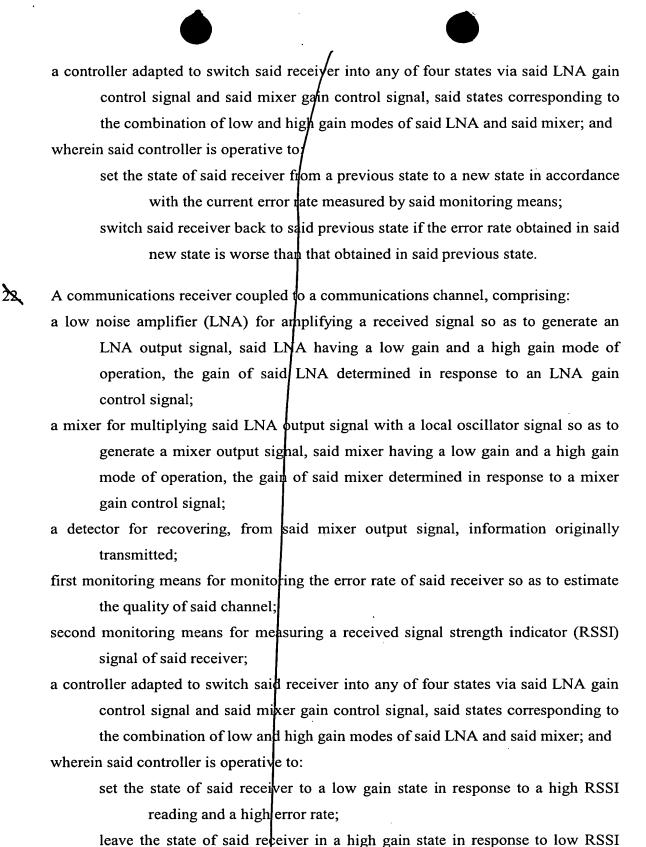
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a data slicer operative to apply a threshold to the output voltage of said discriminator so as to recover digital data therefrom.

- 15. The receiver according to claim 10, wherein said controller is operative to set the LNA to low gain mode and said mixer to low gain mode to achieve maximum linearity and maximum 3<sup>rd</sup> order intercept point.
- 16. The receiver according to claim 10, wherein said controller is operative to set the LNA to low gain mode and said mixer to high gain mode to improve linearity while reducing sensitivity.
- 17. The receiver according to claim 10, wherein said controller is operative to set the LNA to high gain mode and said mixer to low gain mode to achieve greater linearity and higher 3<sup>rd</sup> order intercept point without substantially reducing sensitivity.
  - 18. The receiver according to claim 10, wherein said controller is operative to set the LNA to high gain mode and said mixer to high gain mode to achieve maximum sensitivity.
  - A method of controlling the gain of a Low Noise Amplifier (LNA) and a mixer in a communication receiver, said method comprising the steps of:
    - providing an LNA circuit adapted to have at least two gain settings, namely a low gain mode of operation and a high gain mode of operation;
    - providing a mixer circuit adapted to have at least two gain settings, namely a low gain mode of operation and a high gain mode of operation;
    - setting the LNA to low gain mode and said mixer to low gain mode to achieve maximum linearity, maximum 3<sup>rd</sup> order intercept point and minimum sensitivity;
      - setting the LNA to low gain mode and said mixer to high gain mode to improve linearity while reducing sensitivity;
  - setting the LNA to high gain mode and said mixer to low gain mode to achieve greater linearity and higher 3<sup>rd</sup> order intercept point without substantially reducing sensitivity; and
    - setting said LNA to high gain mode and said mixer to high gain mode to achieve maximum sensitivity.
- 30 20. A communications receiver, comprising:





reading and a high error rate.